

AMENDMENTS TO THE SPECIFICATION

Please amend the TITLE as indicated below:

MECHANISM IN A MICROPROCESSOR FOR EXECUTING NATIVE
INSTRUCTIONS DIRECTLY FROM MEMORY~~TRANSLATOR BYPASS MODE~~
FOR NATIVE INSTRUCTIONS

Please delete the section entitled "SUMMARY" in its entirety and substitute the following section therefor:

A1 SUMMARY OF THE INVENTION

To address the above-detailed deficiencies, it is an object of the present invention to provide a branch prediction apparatus that provides accurate branch predictions by segregating outcome records for conditional branch instructions according to their observed outcome bias.

Please delete the section entitled "ABSTRACT OF THE DISCLOSURE" in its entirety and substitute the following section therefor:

A2 ABSTRACT OF THE DISCLOSURE